

Docket No.: 65933-079

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of : Customer Number: 20277
:
Atsushi WADA, et al. : Confirmation Number:
:
Serial No.: : Group Art Unit:
:
Filed: March 25, 2004 : Examiner:
:

For: ANALOG-TO-DIGITAL CONVERSION CIRCUIT AND IMAGE PROCESSING CIRCUIT FOR
STEPWISE CONVERSION OF A SIGNAL THROUGH MULTIPLE STAGES OF CONVERSION UNITS

CLAIM OF PRIORITY

Mail Stop CPD
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

In accordance with the provisions of 35 U.S.C. 119, Applicants hereby claim the priority of:

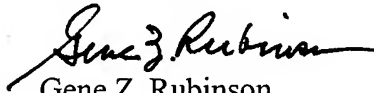
Japanese Patent Application No. JP 2003-085314, filed on March 26, 2003.

Japanese Patent Application No. JP 2003-058546, filed on March 3, 2004.

cited in the Declaration of the present application. Certified copies will be filed in due course.

Respectfully submitted,

MCDERMOTT, WILL & EMERY


Gene Z. Robinson
Registration No. 33,351

600 13th Street, N.W.
Washington, DC 20005-3096
(202) 756-8000 GZR:gav
Facsimile: (202) 756-8087
Date: March 25, 2004